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   Rognstad, M.R.; Naiman, M.;
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   Global Telecommunications Conference, 1999. GLOBECOM '99
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4. **Low-complexity lattice-based low-PAR transmission for DSL channels**
   Collings, I.B.; Clarkson, IV.L.;
   Global Telecommunications Conference, 2003. GLOBECOM '03. IEEE
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5. **A novel DHT-based FFT/IFFT processor for ADSL transceivers**
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   Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on
   Volume 1, 30 May-2 June 1999 Page(s):51 - 54 vol.1
   Digital Object Identifier 10.1109/ISCAS.1999.777803

6. **Efficient VLSI architectures for fast computation of the discrete Fourier transform and its in**
Ching-Hsien Chang; Chin-Liang Wang; Yu-Tai Chang;
Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEE
Volume 48, Issue 11, Nov. 2000 Page(s):3206 - 3216
Digital Object Identifier 10.1109/78.875476

7. **A DHT-based FFT/IFFT processor for VDSL transceivers**
Chin-Liang Wang; Ching-Hsien Chang;
Acoustics, Speech, and Signal Processing, 2001. Proceedings. (ICASSP '01). 2001 IEEE Internatic
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Digital Object Identifier 10.1109/ICASSP.2001.941142

8. **A new memory-based FFT processor for VDSL transceivers**
Chin-Liang Wang; Ching-Hsien Chang;
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9. **A novel memory-based FFT processor for DMT/OFDM applications**
Ching-Hsien Chang; Chin-Liang Wang; Yu-Tai Chang;
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